

Reachability in Networks of Register Protocols under a Stochastic Scheduler

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- 1 Distributed protocols
- 2 Cut-off
- 3 Existence and decision problem
- 4 Conclusion

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Context: distributed systems

Goal

Study distributed systems composed of *many identical components* running concurrently.

Useful for distributed algorithms, ad-hoc networks, communication protocols, etc.

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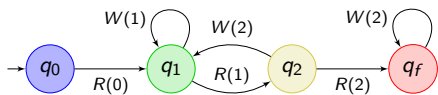
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Study distributed systems composed of *many identical components* running concurrently.

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- Each process is (a copy of) an automaton, a *PDS* ...
- Different means of communication: *Rendez-vous* ([GS92]), *broadcast messages* ([EFM99, DSZ10]), shared register ([ABG15, EGM13])...

Our model: summary



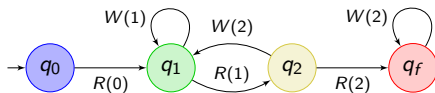
Register protocol with $D = \{0, 1, 2\}$.

Definition: register protocol

$\mathcal{P} = \langle Q, D, q_0, d_0, q_f, T \rangle$

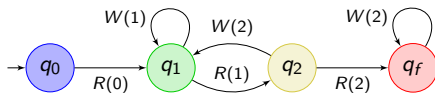
- $\langle Q, q_0, q_f, T \rangle$ is a finite state automaton;
- D finite dataset for the shared register;
- d_0 an initial value;
- $T \subseteq Q \times \{R, W\} \times D \times Q$ set of transitions, labelled by read and write operations over D .

Our model



Network for two processes (self-loops omitted).

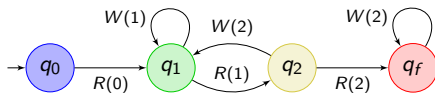
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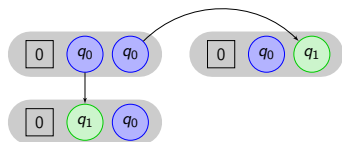
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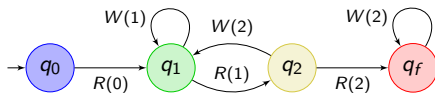
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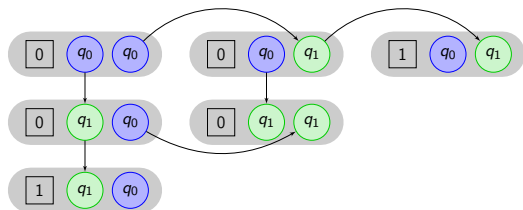
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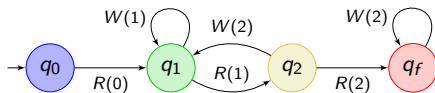
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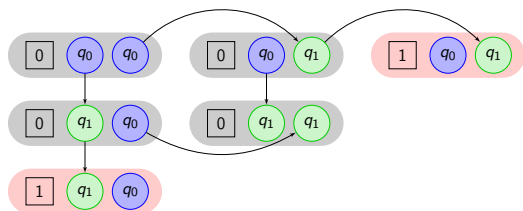
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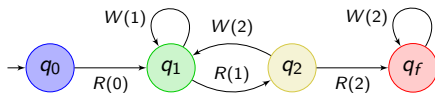


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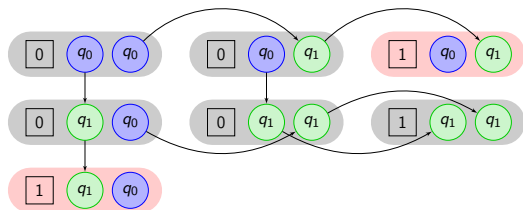


- There exist paths from there, the processes in q_0 are trapped;

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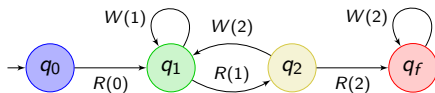
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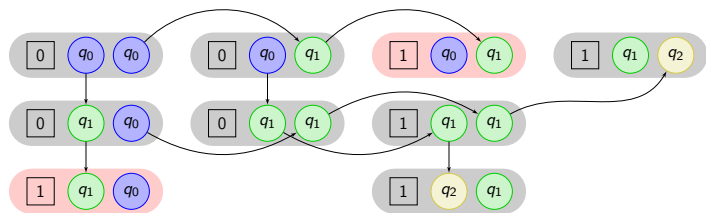
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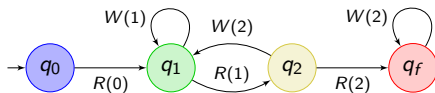
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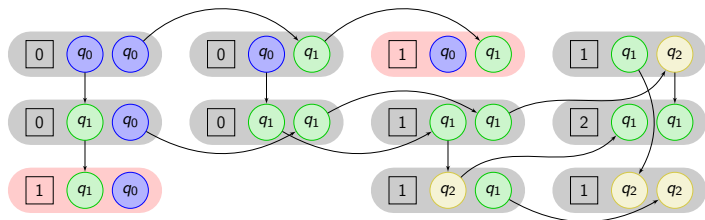
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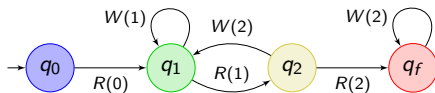
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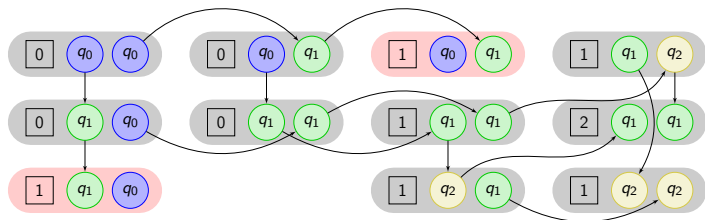
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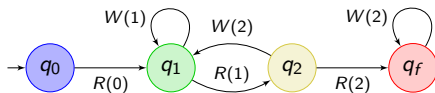
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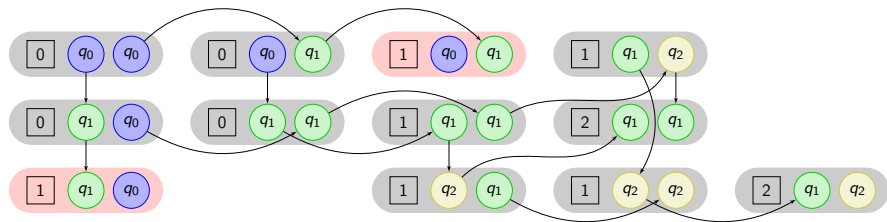
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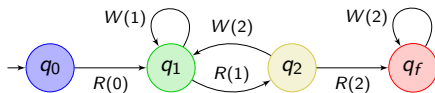
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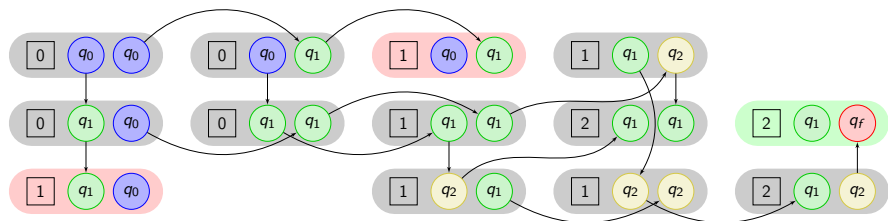
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(Non-exhaustive construction)

- There exist paths from there, the processes in q_0 are trapped;
- There exist paths that reach q_f ...
- ...and they require at least two processes.

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- How does the **scheduler** work ?

Non-deterministic Scheduler Case

- The scheduler is **helpful**;
- **Monotonicity**: if q_f is reachable with n initial processes, it is with $n + 1$;

_____ → n

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- ... and there exists a witness of polynomial size.



Theorem ([EGM13])

Given a protocol, one can decide in PTIME whether there exists a parameter such that q_f is reachable.

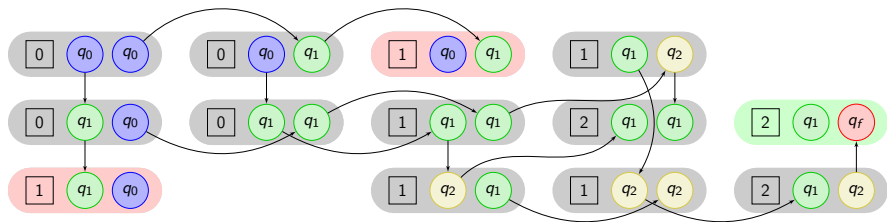
The problem becomes NP-complete, when one of the initial process is distinguished from the others (leader).

Fair, Probabilistic scheduler

- We don't control the scheduler anymore;
- **Stochastic** behaviour (environment);
- Finite patterns cannot be repeated infinitely often;
- We consider **almost-sure** reachability: $\mathbb{P}_n(\diamond \uparrow q_f) \stackrel{?}{=} 1$

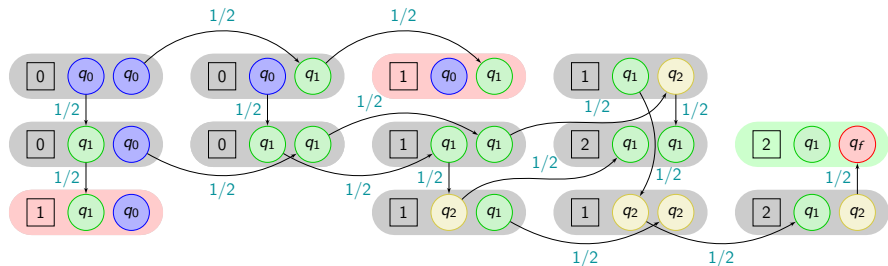
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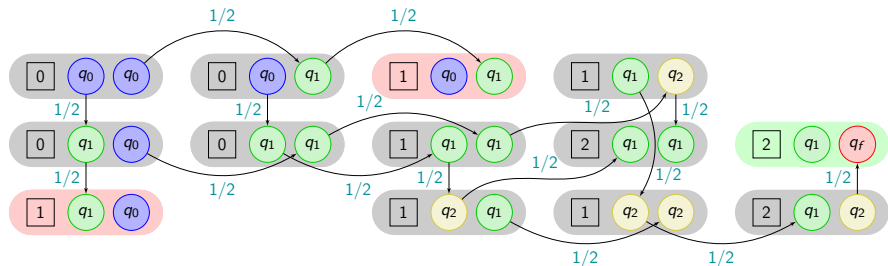
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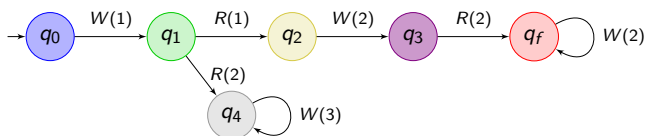
Qualitative property
+
Finite configuration space



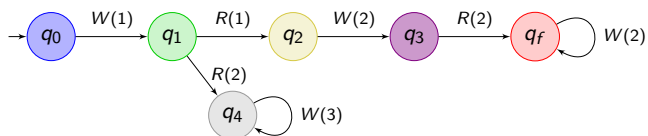
Exact probability values are
not relevant.

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Lack of monotonicity

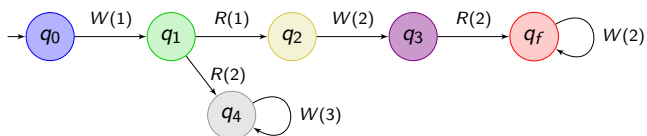


Lack of monotonicity



⇒ **Additional processes can create new deadlocks!**

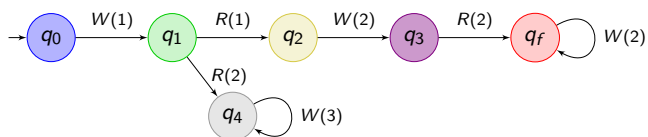
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⇒ We need new techniques to detect such behaviors.

Cut-off

Definition: cut-off

An integer $k \in \mathbb{N}$ is a cut-off for almost-sure reachability for \mathcal{P} if one of the following two properties holds:

- $\forall n \geq k \mathbb{P}_n(\diamond \uparrow q_f) = 1$: **Positive** cut-off;

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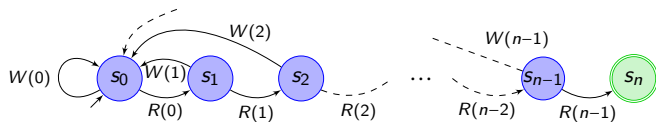
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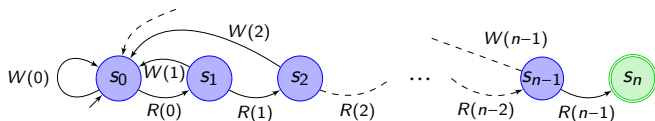
⚠ Cut-offs need not exist *a priori* from the definition

Examples



"Filter" protocol \mathcal{F}_n for $n > 0$.

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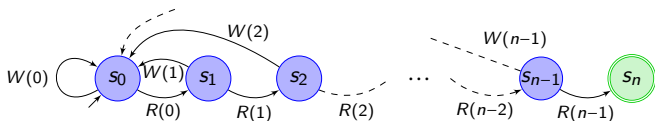
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For protocol \mathcal{F}_n ,

- ▷ networks of size $\geq n$ cover s_n with probability 1,
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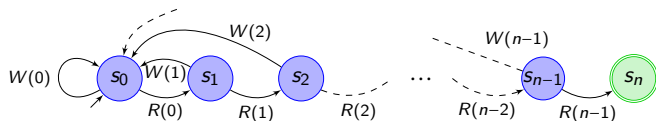
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- ⇒ Tight negative cut-off exponential.

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Existence of a cut-off

Main result

Theorem

For any register protocol \mathcal{P} **there always exists a cut-off for almost-sure reachability**, whose value is at most doubly-exponential in the size of \mathcal{P} . Whether it is a positive or a negative cut-off can be decided in EXPSPACE, and is PSPACE-hard.

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⚠ This result strongly relies on the “regularity-breaking” aspect of our stochastic scheduler and on the non-atomicity of read/write operations.

The non-atomicity guarantees that when a process takes a transition, all processes in the same state can also take the same transition (with a non-zero probability).

⇒ Crucial to obtain a copycat lemma.

Existence: quick sketch

- Write $\mathbf{Pre}^* \subseteq \Gamma$ the set of configurations that can reach q_f ;
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Naive approach: compute $\min \text{Post}^{*\geq 1}$ and $\min \text{Pre}^*$.

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Idea: abstract the system up to the first K processes.

- Almost-sure reachability of q_f **is preserved**;
- Underlying graph still of double-exponential size (multiset representation);
- Reachability in NLOGSPACE [Sip97] w.r.t. the graph \implies NEXPSPACE w.r.t. the protocol \implies **EXSPACE** by Savitch's theorem [Sip97].

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- ▶ Best lower bound for positive cut-offs so far: linear (cf. “filter” protocol).
- ▶ Best lower bound for negative cut-offs so far: exponential.
- ▶ PSPACE-hardness via linear-bounded Turing machine [Sip97]: we build a protocol for which there is a negative cut-off iff the machine reaches its final state q_{halt} .

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Shares idea with the exponential counter

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Some differences with classical models:

- lack of monotonicity in general,
- complexity (PSPACE-hardness while many problems are polynomial or in NP/coNP),
- cut-offs may be exponential (most models admit polynomial cut-offs).

⇒ **Slight changes in the setting induce important changes in complexity.**

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Many thanks! Any question?

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