

Curriculum Vitæ of Blaise GENEST

Permanent full time researcher of CNRS
(Centre National de la recherche scientifique).

Blaise GENEST Born 12/02/78, french E-mail : genest@crans.org

Employement :

Oct 2005-Now Permanent full time researcher of French CNRS in Computer Science.

Jan 2010-Jan 2012 Working at IPAL, a joint French - Singaporian Lab, Singapore.

Oct 2005-Dec 2009 Working at IRISA, Computer Science Lab of Université Rennes 1, France.

Oct 2004- Sept 2005 Postdoctorate at University of Warwick (1 year contract of full time researcher), Computer Science departement, United Kingdom.

Studies :

Jan 2002 - Sep 2004 (Doctorate Level) PhD Thesis of Computer Science at LIAFA (Univ. Paris 7), grant holder and scholarship for 3 years (ended early because of Postdcorate). Defended on the 29th of November 2004.

Title : Verification of Message Sequence Charts.

Teaching : Hardware Architecture, Java, C.

Sept 1999-Dec 2001 (Master Level) Graduate studies at Ecole Normale Supérieure de Cachan (ENS Cachan grant holder and scholarship for 4 years, ended early because of PhD studies), France.

Sep 2000- Dec 2001 (Master 2) Master of Science (Computer Science) in ENS Cachan, Paris 7 and Paris 11, (mark 17.5 / 20)

Options : Hardware Architecture, Cryptography and Arithmetic Algorithms, Automata Theory, Verification and Model Checking.

2 successful Master Thesis :

Verification of Hierarchical Message Sequence Charts, Supervisor : Anca Muscholl (LIAFA, Univ. Paris 7).

Designing a configurable processor with an associated programming language. Supervisor : Olivier Temam (LRI, Univ. Paris 11).

Fev 2000-Jul 2000 Master 1 in Computer Science at ENS Cachan and Paris 7.

Sep 1999-Jan 2000 Licence 3 in Mathematics and Computer Science at ENS Cachan and Paris 7.

Sept 97-Jun 99 (Licence 1 and 2) Preparatory Classes for Engineer Schools, Mathematics, Physics and Computer Science, Orleans and Nantes, France.